

Altera Corporation

Garanti: 12 ay
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¼r¼n	A¼ıklama
Ip-Cic	CIC COMPILER, SOFTWARE APPLICATION:IP CORE, DSP FILTERS AND TRANSFORMS, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLONE, STRATI
Ippcie4	PCI EXPRESS X1 AND X4 LANES, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, PCI, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA
Ci1800lc	
Epm570m256c5n	
Epm7160slc8410n	
Epm7256aeqc208-7n	
Epc1064pc8	
Add-Floatall	QUARTUS II SOFTWARE ADDITIONAL FLOATING-NODE SEAT, SUPPORTED FAMILIES:QUARTUS II, LICENSE TYPE:NETWORKED, SUPPORTED HOSTS:WINDOWS, LINUX, UNIX, SERIES

Epm570t100c4n	
Ep3c25e144c8n	
Ep3c55u484c7n	
Ep3c16e144c8n	
Ep3c16u256c8n	
Ip-Fir	FIR FILTER COMPILER, SOFTWARE APPLICATION:IP CORE, DSP FILTERS AND TRANSFORMS, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLONE,
Ep2c35f484c8n	
Epm570m100c5n	
Epm7128aetc144-7n	
Sw-Quartus-Se-Flt	QUARTUS II FLOATING NODE 1YR SUBSCRIPTION FOR ALL DEVICES ON WIN/LINUX, CORE ARCHITECTURE:CPLD, FPGA, SUPPORTED FAMILIES:QUARTUS II, NIOS II, SOFTWARE
Ep10k100efc484-1n	
Epm570zm144c7n	
Ep600dc-3	
Ep2c20af256i8n	
Ep3c10f256c7n	
Ep20k160eqc208-1x	
Ep1k30tc144-3n	
Ep20k200ebc652-1x	
Epc1pi8n	
Ipr-Slite2	IP CORE RENEWAL OF IP-SLITE2, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, SERIAL, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, HARD
Epm7128sqc100-15n	
Ep1s10b672c6es	
Epm7064sli44-7n	
Ipsw-Tcpip-Nios	
Ipsdramddr	DOUBLE DATA RATE (DDR) SDRAM CONTROLLER, SOFTWARE APPLICATION:IP CORE, MEMORY CONTROLLERS, SDRAM, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECT
Ep10k50vbc356-2	

Ip-Rldramii	SDRAM, SUPPORTED DEVICES:HARDCOPY
Ipr-Cic	IP CORE RENEWAL OF IP-CIC, SOFTWARE APPLICATION:IP CORE, DSP FILTERS AND TRANSFORMS, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLONE, STR
Ipviterbihs	PARALLEL HIGH-SPEED VITERBI DECODER, SOFTWARE APPLICATION:IP CORE, DSP ERROR DETECTION / CORRECTION, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHIT
Epm7128slc84-15	
Iprpcie4	IP CORE RENEWAL OF IP-PCIE/4, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, PCI, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLONE
Ipviterbiss	SERIAL LOW-SPEED VITERBI DECODER, SOFTWARE APPLICATION:IP CORE, DSP ERROR DETECTION / CORRECTION, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECT
Epc16uc88n	
Epm1270m256c5n	
Ep2s15f484c3n	
Epm7128sli84-10n	
Dkstart4cgx15np	
Ep1c3t144c8n	
Ep3c40f484c8n	
Dk-Nios-2s60n	DEV KIT (DEV TOOL), SUPPORTED FAMILIES:STRATIX IINIOS II, TOOL/BOARD
Ep2s60f1020c5n	IC STRATIX II FPGA 550MHZ FBGA-1020, FAMILY TYPE:STRATIX II, SERIES:STRATIX II, NO. OF I/OS:718, I/O SUPPLY VOLTAGE:3.3V, O
Ipsr-Base	IPS-BASE RENEWAL, SOFTWARE APPLICATION:IP CORE, SUITES, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:STRATIX, SUPPORTED FAMILIES:STRATIX FPGA, FEATUR
Epm570gt100c3n	
Swr-Quartus-Se-Fix	QUARTUS II RENEWAL FIXED NODE 1YR SUBSCRIPTION FOR ALL DEVICES ON WINDOWS, CORE ARCHITECTURE:CPLD, FPGA, SUPPORTED FAMILIES:QUARTUS II, NIOS II, SOFTW
5m160zt100c5n	
Ep3c10m164c8n	

Iprviterbihs	IP CORE RENEWAL OF IP-VITERBI/HS, SOFTWARE APPLICATION:IP CORE, DSP ERROR DETECTION / CORRECTION, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA,
Ep2c5f256c8	
Epm7128aetc144-10n	
Epm2210gf324c3n	
Epm570gt144c3n	
Dk-Dev-2agx260n	ARRIA II GX FPGA DEVELOPMENT KIT, 6G EDITION, SILICON MANUFACTURER:ALTERA, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, SILICON CORE NUMBER:EP
Ep2sqx30cf780c4n	
Ep3c40f324c8n	
Epf6010atc144-3n	
Ep1c20f324c8n	
Dk-Maxii-1270n	
Epm570t100c5n	
Ep2sqx90ff1508c5n	IC STRATIX II GX FPGA 550MHZ FBGA1508, SERIES:STRATIX II GX, NO. OF I/OS:650, I/O SUPPLY VOLTAGE:3.3V, OPERATING FREQUENCY
Iprsramddr	IP CORE RENEWAL OF IP-SDRAM/DDR, SOFTWARE APPLICATION:IP CORE, MEMORY CONTROLLERS, SDRAM, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:CYCLONE, HARDC
Epm3032atc44-7n	
Iptr-Dspbuilder	IP CORE RENEWAL OF IPT-DSPBUILDER, SOFTWARE APPLICATION:IP CORE, DSP BUILDER, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLONE, STRATIX, S
Epm7256aetc14410n	
Epf10k10tc144-3n	
Ips-Embedded	ALTERA POPULAR EMBEDDED IP CORE SUITE, SOFTWARE APPLICATION:IP CORE, SUITES, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:CYCLONE, SUPPORTED FAMILIES
Ipr-Posphy4	IP CORE RENEWAL OF IP-POSPHY4, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, COMMUNICATION, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARR
	IP CORE RENEWAL OF IP-VITERBI/SS, SOFTWARE APPLICATION:IP CORE, DSP ERROR DETECTION /

Iprviterbiss	CORRECTION, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA,
Epm570t100c3n	
Epm570f256i5n	
Ippcie1	PCI EXPRESS X1 LANE, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, PCI, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLON
Epf10k20tc144-4n	
Ip-Cpri	COMMON PUBLIC RADIO INTERFACE (CPRI), SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, HIGH SPEED, CORE ARCHITECTURE:FPGA, CORE SUB-AR
Ep2c35f672c8n	
Epf8282atc100-4n	
Ipagxpcie4	PCI EXPRESS X1 AND X4 LANES FOR ARRIA GX, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, PCI, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHI
Ep2sgx60ef1152c5n	IC STRATIX II GX FPGA 550MHZ FBGA1152, SERIES:STRATIX II GX, NO. OF I/OS:534, I/O SUPPLY VOLTAGE:3.3V, OPERATING FREQUENCY
Epm240zm100c6n	
Epm570t144c4n	
Ep2s30f672c3n	IC STRATIX II FPGA 550MHZ FBGA-672, NO. OF MACROCELLS:33880, FAMILY TYPE:STRATIX II, SERIES:STRATIX II, NO. OF I/OS:500, C
Epf10k10aqc208-3n	
Iprpcit64	IP CORE RENEWAL OF IP-PCI/T64, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, PCI, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLON
Iptr-C2h-Nios	IP CORE RENEWAL OF IPT-C2H-NIOS, SOFTWARE APPLICATION:IP CORE, NIOS PROCESSOR AND FUNCTIONS, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:CYCLONE, RO
Epc2lc20n	
Ep3c5f256c7n	
Ep3c80f780c8n	
Ep1sgx25ff1020c7	LOGIC ANALIZER
Epm570zm256c6n	

Dk-Video-4sgx230n	AUDIO VIDEO DEVELOPMENT KIT, STRATIX IV GX EDITION, SILICON MANUFACTURER:ALTERA, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:STRATIX, SILICON CORE N
Iprpcimt64	IP CORE RENEWAL OF IP-PCI/MT64, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, PCI, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLO
Epm2210gf256c5n	
5m160zm100c5n	
Epm3064atc100-10n	
Ipr-Asi	IP CORE RENEWAL OF IP-ASI, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, AUDIO AND VIDEO, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA
Epm7032sli44-7n	
Epm3128atc100-5n	
Ippcie8	PCI EXPRESS X1 X4 AND X8 LANES, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, PCI, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:AR
Ep1k10qc208-3	
Dk-N2eval-3c25n	
Ip-Crc	CRC COMPILER, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, COMMUNICATION, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYC
Epm3032atc44-4n	
Ipr-Rldramii	SDRAM, SUPPORTED DEVICES:HARDCOPY
Epf10k20rc208-3n	
Iprsdrampddr2	IP CORE RENEWAL OF IP-SDRAM/HDDR2, SOFTWARE APPLICATION:IP CORE, MEMORY CONTROLLERS, SDRAM, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLO
Epm7064aetc44-4	
Ep3c25f256c8n	
8259266-901	PC MODULE
Epm570f256c4n	
Ep1k100qc2081n	
Epm3032atc44-10n	

Ip-Rsenc	REED-SOLOMON ENCODER, SOFTWARE APPLICATION:IP CORE, DSP ERROR DETECTION / CORRECTION, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, C
Dk-Dev-1agx60n	DEV KIT (DEV TOOL), SUPPORTED FAMILIES:ARRIA GX, TOOL/BOARD APPLICATION:GENERAL
Epcs1si8n	
Ip-Sdi	SDI VIDEO INTERFACE, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, AUDIO AND VIDEO, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:A
Ipr-Sdi	IP CORE RENEWAL OF IP-SDI, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, AUDIO AND VIDEO, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA
Iprsdramddr2	IP CORE RENEWAL OF IP-SDRAM/DDR2, SOFTWARE APPLICATION:IP CORE, MEMORY CONTROLLERS, SDRAM, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:CYCLONE, HARD
Epm2210gf324c5n	
Ep1c6f256c8n	
Ep2c8f256c8n	
Ep2c15af256c8n	
Epf6016atc144-3n	
Epm240zm68c7n	
Epm7064aeti44-7n	
Ippcimt32	32-BIT 66MHZ PCI MASTER/TARGET, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, PCI, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:AR
Ep20k400ebc652-1	GATE ARRAY FIELD PROGRAMMABLE
Sw-Qii-Separation	QUARTUS II SEPARATION ADD-ON 1YR SUBSCRIPTION, SERIES:QUARTUS II, SUPPORTED DEVICES:ALL ALTERA DEVICES, TOOL FUNCTION:CIRCUIT DESIGN AND SIMULATION SO
Ep1c3t100c8n	
Udn2985a	
Ep2c15af484c8n	
Epm1270f256i5n	
Ipsr-Video	RENEWAL OF IPS-VIDEO, SOFTWARE APPLICATION:IP CORE, SUITES, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLONE, STRATIX, SUPPORTED

FAMILIES:	
Ipr-Xauipcs	IP CORE RENEWAL OF IP-XAUIPCS, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, ETHERNET, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:STRATIX
Epm7064slc44-10n	
Epm7160sti100-10n	
Dk-Dev-4sgx230n	STRATIX IV GX FPGA DEVELOPMENT KIT, SILICON MANUFACTURER:ALTERA, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:STRATIX, SILICON CORE NUMBER:EP4S, SILI
Epm7032aelc44-10n	
Epm3128atc144-5n	
Dk-Nios-2c35n	DEV KIT (DEV TOOL), SUPPORTED FAMILIES:NIOS II CYCLONE II, SUPPORTED DEVICES:EP2C35F672
Ipsdramhpddr	DDR SDRAM HIGH PERFORMANCE CONTROLLER, SOFTWARE APPLICATION:IP CORE, MEMORY CONTROLLERS, SDRAM, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTUR
Ep1k10qc208-2n	
Ipr-Crc	IP CORE RENEWAL OF IP-CRC, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, COMMUNICATION, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA,
Ipt-C2h-Nios	NIOS II C-TO-HARDWARE ACCELERATION COMPILER, SOFTWARE APPLICATION:IP CORE, NIOS PROCESSOR AND FUNCTIONS, CORE ARCHITECTURE:FPGA, CORE SUB-AR
Epm7064stc44-7n	
Epm570zm256c7n	
Ep1c4f400c8n	
Ipr-10gethmac	IP CORE RENEWAL OF IP-10GETHMAC, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, ETHERNET, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:STRATI
Dk-Dev-3sl150n	STRATIX III FPGA DEVELOPMENT KIT, SILICON MANUFACTURER:ALTERA, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:STRATIX, SILICON CORE NUMBER:EP3S, SILICO
Ep1s10f780c5es	FPGA LOGIC IC, PROGRAMMABLE LOGIC TYPE:FPGA, LOGIC TYPE:PROGRAMMABLE, FREQUENCY MAX:422MHZ, SUPPLY VOLTAGE MIN:1.425V, SUPPLY VOLTAGE MAX:1.575V, TERM
Ipr-Nco	IP CORE RENEWAL OF IP-NCO, SOFTWARE APPLICATION:IP CORE, DSP MODULATION / DEMODULATION, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA,

CYCLONE,	
Epf10k30aqc240-3n	
Ep3c16f484c7n	
Ep2sgx60df780c5n	IC STRATIX II GX FPGA 550MHZ FBGA-780, FAMILY TYPE:STRATIX II GX, SERIES:STRATIX II GX, NO. OF I/OS:364, CLOCK MANAGEMENT:DLL, PLL, I/O SUPPLY VOLTAG
Epm9560ri240-20	
Epm1270gt144c5n	
Epm570f100i5n	
Epm240zm68c6n	
Epf10k30efc256-3n	
Epf10k30etc144-1n	
5m80ze64c5n	
Epm7128stc100-10n	
Epm570zm144c6n	
Dk-Start-4cgx15n	
5m80zt100c5n	
Ip-Asi	ASI VIDEO INTERFACE, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, AUDIO AND VIDEO, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:A
Epl1s10b672c7es	
Epc1441tc32n	
Epl1s80b956c7	GATE ARRAY FIELD PROGRAMMABLE STRATIX I 7904
Epm2210f256c4n	
Epm7064tc447	
Eplagx50df1152c6n	
Ipr-Fft	IP CORE RENEWAL OF IP-FFT, SOFTWARE APPLICATION:IP CORE, DSP FILTERS AND TRANSFORMS, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLONE, STR
Dk-Dev-5m570zn	
Dk-Dev-2agx125n	ARRIA II GX FPGA DEVELOPMENT KIT, SILICON MANUFACTURER:ALTERA, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, SILICON CORE NUMBER:EP2, SILICON F

Sw-Quartus-Se-Add	QUARTUS II FLOATING NODE ADDITIONAL SEAT FOR ALL DEVICES ON WIN/LINUX, CORE ARCHITECTURE:CPLD, FPGA, SUPPORTED FAMILIES:CYCLONE, ARRIA, MAX, STRATIX
Iprpcie1	IP CORE RENEWAL OF IP-PCIE/1, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, PCI, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLONE
Epf8452agc160-3	
Ep3sl200f1152c3	FIELD PROGRAMMABLE GATE ARRAY
Ips-Video	MEGACORE SUITE W/ 17 DSP VIDEO/IMAGE PROCESSING FUNCTIONS, SOFTWARE APPLICATION:IP CORE, SUITES, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA,
Ippcit32	32-BIT 66MHZ PCI TARGET, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, PCI, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CY
Epm7128aetc100-7n	
Ep2sgx30cf780c5n	
Ip-Fft	FFT/IFFT, SOFTWARE APPLICATION:IP CORE, DSP FILTERS AND TRANSFORMS, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLONE, STRATIX, S
Ep2s180f1020c3n	IC STRATIX II FPGA 550MHZ FBGA-1020, FAMILY TYPE:STRATIX II, SERIES:STRATIX II, NO. OF I/OS:742, I/O SUPPLY VOLTAGE:3.3V, O
Ep1s25f780c6n	IC STRATIX FPGA 420MHZ FBGA-780, NO. OF MACROCELLS:25660, FAMILY TYPE:STRATIX, SERIES:STRATIX, NO. OF I/OS:597, CLOCK MANAGEMENT:DLL, PLL, I/O SUPPLY
Ep2s30f484c5n	
Ep20k400efc672-2x	GATE ARRAY FIELD PROGRAMMABLE
Dk-Dev-3cls200n	CYCLONE III LS FPGA DEVELOPMENT KIT, SILICON MANUFACTURER:ALTERA, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:CYCLONE, SILICON CORE NUMBER:EP3C, SIL
Epf6024aqc240-3n	
Ipsdramddr3	DDR3 SDRAM HIGH PERFORMANCE CONTROLLER, SOFTWARE APPLICATION:IP CORE, MEMORY CONTROLLERS, SDRAM, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTU
Epm240gm100c5n	
Ippqdriiuni	QDR II / QDR II+ SRAM CONTROLLER WITH UNIPHYS, SOFTWARE APPLICATION:IP CORE, MEMORY CONTROLLERS, SRAM, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITE

Ip-Nios	
Ep1s10f672c6es	
Epm570f100c4n	
Epm570t144c3n	
Sw-Quartus-Se-Fix	QUARTUS II FIXED NODE 1YR SUBSCRIPTION FOR ALL DEVICES ON WINDOWS, CORE ARCHITECTURE:CPLD, FPGA, SUPPORTED FAMILIES:QUARTUS II, NIOS II, SOFTWARE EDIT
Ep3c40f780c8n	
Epm7064stc44-10n	
Ep3c25f324c8n	
Ipsdramddr2	DDR2 SDRAM CONTROLLER, SOFTWARE APPLICATION:IP CORE, MEMORY CONTROLLERS, SDRAM, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:CYCLONE, HARDC
Dk-Dev-3c120n	CYCLONE III FPGA DEVELOPMENT KIT, SILICON MANUFACTURER:ALTERA, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:CYCLONE, SILICON CORE NUMBER:EP3C, SILICO
Ep1agx60ef1152c6n	
5m2210zf324c5n	
Iprldiuni	IP CORE RENEWAL OF IP-RLDII/UNI, SOFTWARE APPLICATION:IP CORE, MEMORY CONTROLLERS, SDRAM, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, HARD COP
Epm570zm100c7n	
Ep8282alc84-4n	
Epm570t144c5n	
Epm7096lc68-12	
Ep1s20f780c7n	
Ipr-Firii	IP CORE RENEWAL OF IP-FIRII, SOFTWARE APPLICATION:IP CORE, DSP FILTERS AND TRANSFORMS, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLONE, S
Ep1sqx10cf672c6n	
Iprpcie8	IP CORE RENEWAL OF IP-PCIE/8, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, PCI, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLONE
Ep3c5m164c8n	

Ep2c50f672c8n	
Iprpcimt32	IP CORE RENEWAL OF IP-PCI/MT32, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, PCI, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLO
Ep1k50tc144-1n	
Epc1441lc20n	
Ep3sl150f1152c4n	IC STRATIX III FPGA 717MHZ FBGA-1152, SERIES:STRATIX III, TOTAL RAM BITS:6390KBIT, OPERATING FREQUENCY MAX:717MHZ, OPERATING
Epm240t100c4n	
Epm3128atc100-7n	
Ep2sgx60cf780c4n	INT CIRCUIT
Ep3c25q240c8n	
Ep1k50qc208-2n	
Ep1c12f324c8n	
Ep3c40f780c7n	
Epm7128sqc100-10n	
Ipt-Dspbuilder	MATLAB/SIMULINK INTERFACE, SOFTWARE APPLICATION:IP CORE, DSP BUILDER, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCL
Epm3128atc100-10n	
Ep3c5f256c8n	
Epm240t100c3n	
Ep1c20f400c8n	
Ic18110lc20t	
Ep2sgx30df780c3n	IC STRATIX II GX FPGA 550MHZ FBGA-780, FAMILY TYPE:STRATIX II GX, SERIES:STRATIX II GX, NO. OF I/OS:361, CLOCK MANAGEMENT:D
Epm7064slc44-10	
Epm7160stc100-10n	
Ep3c120f484c8n	
Ep1k100qc208-3n	
Ep2c8t144c8n	
Ep1c6q240c6n	

Ep2s90f1020c5n	IC STRATIX II FPGA 550MHZ FBGA-1020, FAMILY TYPE:STRATIX II, SERIES:STRATIX II, NO. OF I/OS:758, I/O SUPPLY VOLTAGE:3.3V, O
Ipr-Cpri	IP CORE RENEWAL OF IP-CPRI, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, HIGH SPEED, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CY
Ipr-Riophy	IP CORE RENEWAL OF IP-RIOPHY, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, HIGH SPEED, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA,
Ep3c10f256c8n	
Epm7160eqc160-15	
Ep2aqx125ef35c6es	IC ARRIA II GX FPGA 350MHZ FBGA-1152, LOGIC FAMILY:ARRIA II GX, NO. OF SPEED GRADES:6, SERIES:ARRIA II GX, TOTAL RAM BITS:8121KBIT, NO. OF I/OS:452
Ep1c3t144c7	
Ep2s180f1508c5n	IC STRATIX II FPGA 550MHZ FBGA-1508, FAMILY TYPE:STRATIX II, SERIES:STRATIX II, NO. OF I/OS:1170, I/O SUPPLY VOLTAGE:3.3V
Epm3256atc144-7n	
Epm5128lc	
Ep3c10e144c8n	
Ip-Xauipcs	XAUI PHY, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, ETHERNET, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:STRATIX, SUPPORTED
Epf10k50vrc240-3	
Epf10k50bc356-3	
Ep1800lc	
Epm7032lc44-2	
Dk-Emb-3c120n	CYCLONE III EMBEDDED SYSTEMS DEVELOPMENT KIT, SILICON MANUFACTURER:ALTERA, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:CYCLONE, SILICON CORE NUMBER:
Ep1s10f780c7es	
Pl-Eth2-Blaster	
Swr-Quartus-Se-Flt	QUARTUS II RENEWAL FLOATING NODE 1YR SUBSCRIPTION FOR ALL DEVICES ON WIN/LINUX, CORE ARCHITECTURE:CPLD, FPGA, SUPPORTED FAMILIES:QUARTUS II, NIOS II

Ipr-Fir	IP CORE RENEWAL OF IP-FIR, SOFTWARE APPLICATION:IP CORE, DSP FILTERS AND TRANSFORMS, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLONE, STR
Eplagx90ef1152c6n	IC ARRIA GX FPGA 622.08MHZ FBGA-1152, FAMILY TYPE:ARRIA GX, SERIES:ARRIA GX, NO. OF I/OS:538, I/O SUPPLY VOLTAGE:3.3V, OPE
Ipr-Hpmcii	SDRAM, SUPPORTED DEVICES:ARRIA, HARDCOPY, STR
Floatall	QUARTUS II SOFTWARE FLOATING-NODE SUBSCRIPTION, SUPPORTED FAMILIES:QUARTUS II, LICENSE TYPE:NETWORKED, SUPPORTED HOSTS:WINDOWS, LINUX, UNIX, SERIES:QU
Epm240m100c5n	
Sw-Modelsim-Ae	MODELSIM FOR QUARTUS II 1YR SUBSCRIPTION FLOAT/FIXED FOR WIN/LINUX, SOFTWARE APPLICATION:FPGA DESIGN, SIMULATION, SOFTWARE EDITION:STANDARD, SUPPORTED
Ippcimt64	64-BIT 66MHZ PCI MASTER/TARGET, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, PCI, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:AR
Epm7032aetc44-7n	
Epm3064alc44-10n	
Dk-Dsp-3sl150n	STRATIX III DSP DEVELOPMENT KIT, SILICON MANUFACTURER:ALTERA, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:STRATIX, SILICON CORE NUMBER:EP3S, SILICON
Epls10f780c6es	
Eplsgx40gf1020c6n	IC STRATIX GX FPGA 650MHZ FBGA-1020, FAMILY TYPE:STRATIX GX, SERIES:STRATIX GX, NO. OF I/OS:362, I/O SUPPLY VOLTAGE:3.3V
Epm7128sqc160-15n	
Ipsramqdrii	QDR II SRAM CONTROLLER, SOFTWARE APPLICATION:IP CORE, MEMORY CONTROLLERS, SDRAM, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:HARDCOPY, STRA
Iprsdramhpddr	IP CORE RENEWAL OF IP-SDRAM/HPDDR, SOFTWARE APPLICATION:IP CORE, MEMORY CONTROLLERS, SDRAM, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLO
Epf10k10qc208-3n	
Epm3064ati44-10n	
Epm3128atc144-7n	

Epm1270gt144c4n	
Iprqdriiuni	
Ip-Rsdec	REED-SOLOMON DECODER, SOFTWARE APPLICATION:IP CORE, DSP ERROR DETECTION / CORRECTION, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, C
Ep1k10tc144-3n	
Ep20k1000ebc652-2s	GATE ARRAY FIELD PROGRAMMABLE APEX 20K
Ip-10gethmac	10-GBPS ETHERNET MAC, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, ETHERNET, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:STRATIX
Ipagxpcie1	X1 LANE PCI EXPRESS FOR ARRIA GX, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, PCI, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:
Ipr-Triethernet	
Ep3c16u484c8n	
Ip-Riophy	RAPIDIO 1X/4X SERIAL TRANSPORT LOGICAL, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, HIGH SPEED, CORE ARCHITECTURE:FPGA, CORE SU
Ep2sgx130gf1508c4n	IC STRATIX II GX FPGA 550MHZ FBGA1508, SERIES:STRATIX II GX, NO. OF I/OS:734, I/O SUPPLY VOLTAGE:3.3V, OPERATING FREQUENCY
Ep2sgx60cf780c5n	IC STRATIX II GX FPGA 550MHZ FBGA-780, FAMILY TYPE:STRATIX II GX, SERIES:STRATIX II GX, NO. OF I/OS:364, CLOCK MANAGEMENT:DLL, PLL, I/O SUPPLY VOLTAG
Epm7160sqc160-10n	
Ep1sgx10df672c7	
Epm7256btc100-10	
Dk-Si-4sgx230n	STRATIX IV GX TRANSCEIVER SIGNAL INTEGRITY DEVELOPMENT KIT, SILICON MANUFACTURER:ALTERA, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:STRATIX, SILICO
Epc16qc100n	
Ip-10gbaserpcs	10GBASE-R PHY, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, ETHERNET, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:STRATIX, SUPPO
Iprsrampdrii	
Ep1s25f672c7n	
Epf10k100abc3562	

Ip-10gethernet	10 GBPS ETHERNET MAC PCS PMA REFERENCE DESIGN, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, ETHERNET, CORE ARCHITECTURE:FPGA, CORE
Ipsdramhpddr2	DDR2 SDRAM HIGH PERFORMANCE CONTROLLER, SOFTWARE APPLICATION:IP CORE, MEMORY CONTROLLERS, SDRAM, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTU
Epm570m100i5n	
Ep3c55f484c7n	
Epm1270f256c3n	
Ep3c55f780c8n	
Ep3c25e144c8nes	
PI-Usb-Blaster-Rcn	
Dk-Dsp-3c120n	CYCLONE III DSP DEVELOPMENT KIT, SILICON MANUFACTURER:ALTERA, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:CYCLONE, SILICON CORE NUMBER:EP3C, SILICON
Epm1270f256c5n	
Epm2210f256c3n	
Ip-Ed8b10b	8B10B ENCODER/DECODER, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, COMMUNICATION, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:A
Ip-Hpmcii	HIGH PERFORMANCE SDRAM CONTROLLER II, SOFTWARE APPLICATION:IP CORE, MEMORY CONTROLLERS, SDRAM, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE
Ips-Base	ALTERA IP CORE BASE SUITE, SOFTWARE APPLICATION:IP CORE, SUITES, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:STRATIX, SUPPORTED FAMILIES:STRATIX FPG
Epm570t100i5n	
Epm7064sti100-7n	
Epm1270t144c5n	
Epf8636alc84-4n	
Iprpcit32	IP CORE RENEWAL OF IP-PCI/T32, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, PCI, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLON
Ip-Slite2	SERIALLITE II MEGACORE 1-16 LANE (6.375 GBPS), SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, SERIAL, CORE ARCHITECTURE:FPGA, CORE
	IC STRATIX GX FPGA 650MHZ FBGA-672, NO. OF

Ep1sgx25cf672c7n	MACROCELLS:25660, FAMILY TYPE:STRATIX GX, SERIES:STRATIX GX, NO. OF I/OS:455, CLOCK MANAGEMENT:DLL, PLL, I
Ep1c4f324c8n	
Epm3256aqc208-7n	
Dk-Cycii-2c20n	
Epm3032alc44-10n	
Epm2210f256c5n	
Ep2s30f484c3n	
Ipr-Ed8b10b	IP CORE RENEWAL OF IP-ED8B10B, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, COMMUNICATION, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARR
Epm3256atc144-10n	
Ep1c12f256c8n	
Dk-Dev-4se530n	STRATIX IV E FPGA DEVELOPMENT KIT, SILICON MANUFACTURER:ALTERA, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:STRATIX, SILICON CORE NUMBER:EP4S, SILIC
Epf10k50eqc240-3n	
Epm3064atc44-10n	
Ipr-Rsdec	IP CORE RENEWAL OF IP-RSDEC, SOFTWARE APPLICATION:IP CORE, DSP ERROR DETECTION / CORRECTION, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCL
Epm7128stc100-7n	
Dk-Video-2c70n	DEV KIT (DEV TOOL), SUPPORTED DEVICES:EP2C70F672C6, TOOL/BOARD APPLI
Epc1213lc20	
Epm1270t144i5n	
Ep2a15f672c8	
Ep2c15af256i8n	
Epcs16si16n	
Ip-Firii	FIR FILTER COMPILER II, SOFTWARE APPLICATION:IP CORE, DSP FILTERS AND TRANSFORMS, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCLO
Epm570zm100c6n	

Epm1270gm256c5n	
Ep3c10u256c8n	
Epf10k50vrc240-3n	
Ep3c16f484c8n	
Epf10k20tc144-3n	
Ep3c25u256c8n	
5sgxea7n2f45c2n	3PACK
Swr-Qii-Separation	QUARTUS II SEPARATION ADD-ON 1YR SUBSCRIPTION RENEWAL, SERIES:QUARTUS II, SUPPORTED DEVICES:ALL ALTERA DEVICES, TOOL FUNCTION:CIRCUIT DESIGN AND SIMUL
Epm3064atc100-10	
Epm7128sti100-10n	
Epm7128sqc100-7n	
Epm7192sqi160-10n	
Epm3256aqc208-10n	
Epm7256aetc144-7n	
Ep2agx125ef35c5nes	IC ARRIA II GX FPGA 350MHZ FBGA-1152, NO. OF MACROCELLS:118143, FAMILY TYPE:ARRIA II GX, NO. OF SPEED GRADES:5, SERIES:ARRIA II GX, TOTAL RAM BITS:812
Epm3064atc100-4n	
Ip-Posphy4	POS-PHY LEVEL 4 SPI 4.2 INTERFACE, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, COMMUNICATION, CORE ARCHITECTURE:FPGA, CORE SUB-AR
Epc16qi100n	
Epm7128sqi160-10n	
Ep2c35u484i8n	
Epm7192sqc160-10n	
Ipr-10gethernet	IP CORE RENEWAL OF IP-10GETHERNET, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, ETHERNET, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRI
Dk-Dsp-2s60n	DEV KIT (DEV TOOL), SUPPORTED FAMILIES:STRATIX II, TOOL/BOARD APPLIC
Ep3c40q240c8n	
5m570zt144c5n	

Ipr-Rsenc	IP CORE RENEWAL OF IP-RSENC, SOFTWARE APPLICATION:IP CORE, DSP ERROR DETECTION / CORRECTION, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CYCL
Ep1k100qc-208	
Epcs16si8n	
Ep1c20f400c6	
Ep1c6q240c8n	
Epf10k50sqc240-1n	
Ep2agx125df25c6nes	IC ARRIA II GX FPGA 350MHZ FBGA-572, NO. OF MACROCELLS:118143, FAMILY TYPE:ARRIA II GX, NO. OF SPEED GRADES:6, SERIES:ARRIA
Swr-Modelsim-Ae	MODELSIM FOR QUARTUS II 1YR SUBSCRIPTION RENEWAL FLOAT/FIXED FOR WIN/LINUX, SOFTWARE APPLICATION:FPGA DESIGN, SIMULATION, SOFTWARE EDITION:STANDARD, S
Ipr-10gbaserpcs	IP CORE RENEWAL OF IP-10GBASERPCS, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, ETHERNET, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:STRA
P1042v3179063	
Dk-Si-4s100g2n	TRANSCEIVER SIGNAL INTEGRITY DEVELOPMENT KIT, STRATIX IV GT EDITION, SILICON MANUFACTURER:ALTERA, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:STRATI
Ip-Nco	NUMERICALLY CONTROLLED OSCILLATOR COMPILER, SOFTWARE APPLICATION:IP CORE, DSP MODULATION / DEMODULATION, CORE ARCHITECTURE:FPGA, CORE SUB-AR
Epm240zm100c7n	
Epm240f100c5n	
Epm7128sqi100-10n	
Iprldiuni	RLDRAM II CONTROLLER WITH UNIPHY, SOFTWARE APPLICATION:IP CORE, MEMORY CONTROLLERS, SDRAM, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARR
Ippcit64	64-BIT 66MHZ PCI TARGET, SOFTWARE APPLICATION:IP CORE, INTERFACE AND PROTOCOLS, PCI, CORE ARCHITECTURE:FPGA, CORE SUB-ARCHITECTURE:ARRIA, CY
Ep1s30f780c7n	IC STRATIX FPGA 420MHZ FBGA-780, NO. OF MACROCELLS:32470, FAMILY TYPE:STRATIX, SERIES:STRATIX, NO. OF I/OS:597, CLOCK MANAGEMENT:DLL, PLL, I/O SUPPLY
Epm3064atc44-4n	

[Ep2s60f1020c3n](#)

IC STRATIX II FPGA 550MHZ FBGA-1020, FAMILY
TYPE:STRATIX II, SERIES:STRATIX II, NO. OF I/OS:718, I/O
SUPPLY VOLTAGE:3.3V, 0

[Epm2210f324i5](#)